

L Number	Hits	Search Text	DB	Time stamp
1	3	"472818-\$".did.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/07 11:16
	329	((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 15:25
	0	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and ((map\$4 or convert\$3) near4 (physical coordinate "XY") near6 (logical or binary or bits)) same (row same column))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 15:17
	2	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and ((map\$4 or convert\$3) same (physical coordinate "XY") same (logical or binary or bits)) same (row same column))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 15:19
	16	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and (logical near4 address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 15:29
	678	((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 16:31
	28	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))) and (logical near4 address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 15:57
	0	"5633877".pn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 15:57
	2	"5633877".pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 16:23
	1		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 15:58
	374	((physical near3 address near map\$4) same (logical near3 address)) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 16:25
	1188	((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 16:28
	1188	((((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 16:28

	24	(((((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory)) and (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function)) "6001662".pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 16:39
	2		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/05 17:14
	0	"US-20030167428-\$".did.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/05 17:33
	2	"20030167428-\$".did.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/07 11:15